CprE 381 Toolflow manual

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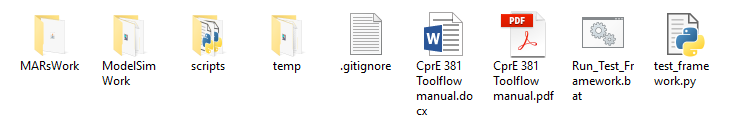
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# Testing Framework

The goal of the test framework is to allow students to compare the output of their processor with that of MARS.

## Getting Started

Opening the test framework, you should have the following files present.

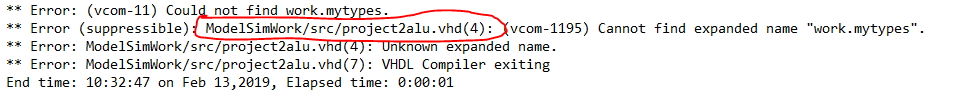


1. Copy all the source files from your processor into the ModelSimWork/src. There should already be a file named tb\_SimplifiedMIPSProcessor.vhd, you do not need to edit this file, and it should not be removed
2. Double-click on Run\_Test\_Framework.bat.
3. You will be prompted for the path to a MIPS assembly file to run. Example programs are provided in MarsWork/Examples, you may however provide the path to any assembly file you like.
4. The framework should now compile, simulate, and show you the differences between your processor’s output in the expected file printed to the command line

## Troubleshooting and FAQ

**Q:** I defined some types in a separate file, so now the program won’t compile unless that file is compiled first. How do I fix this?

**A:** First run the program to find the first file where the error occurs:

Then copy the entire contents of the VHDL file with the types onto the top the file with the error. Delete the types file:

**library** ieee**;**

**use** ieee**.**std\_logic\_1164**.all;**

**package** mytypes **is**

**type** vector\_32\_array **is** **array** **(**0 **to** 31**)** **of** std\_logic\_vector**(**31 **downto** 0**);**

**end** mytypes**;**

**library** IEEE**;**

**use** IEEE**.**std\_logic\_1164**.all;**

**use** IEEE**.**std\_logic\_unsigned**.all;**

**use** work**.**mytypes**.all;**

**entity** project2alu **is**

**port(**

**...**

## Options

The test framework has several flags that can be used to streamline your test framework experience. These flags can be added to line 25 of Run\_Test\_Framework.bat

Example:

**%python\_path%** test\_framework.py --nocompile --asm-file aaa/bbb.asm

Options:

|  |  |
| --- | --- |
| Command | Description |
| --asm-file | Accepts a unix-style path to an assembly file to run. If the provided file cannot be simulated the script prompts the user anyways. |
| --nocompile | Skips recompilation to save time when running multiple assembly files without changing the processor. |
| --max-mismatches | Accepts a natural number which is the number of mismatches between the expected output and your output before the script stops. Default = 5 |
| --sim-timeout | Accepts a natural number which is the number of seconds before the simulation is interrupted. Default=30 |

## Portability

This framework is only supported on the Windows lab computers and ISU VDI ECpE Student.